

400G QSFP-DD FR4/LR4 Optical Transceiver

FEATURES

- product targeted for use in optical interconnects for data communications applications. The high bandwidth module supports 400G Ethernet connections via LC connector over parallel single-mode fiber links up to 2 km and 10 km for the FR4 and LR4 variants.

ABSOLUTE MAXIMUM RATINGS

RECOMMENDED OPERATING CONDITIONS

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TRANSMITTER OPTICAL OUTPUT CHARACTERISTICS

Symbol	Unit	Min	Typ	Max	Notes
P_{out}	dBm	28	-	-	
λ_c	nm	1550	-	-	
$\Delta\lambda$	nm	0	-	-	
T_d	ps	0	25.04	3.5	
Extinction Ratio	dB	15	-	-	
$\text{Polarization Extinction Ratio}$	dB	15	-	-	
$\text{Polarization Loss Difference}$	dB	0	-	-	
$\text{Polarization Dependent Loss}$	dB	0	-	-	
$\text{Polarization Dependent Gain}$	dB	0	-	-	
$\text{Polarization Dependent Attenuation}$	dB	0	-	-	
$\text{Polarization Dependent Dispersion}$	ps/nm	0	-	-	
$\text{Polarization Dependent Delay}$	ps	0	-	-	
$\text{Polarization Dependent Loss Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Gain Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Attenuation Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Dispersion Slope}$	ps/nm	0	-	-	
$\text{Polarization Dependent Delay Slope}$	ps/nm	0	-	-	
$\text{Polarization Dependent Loss Slope Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Gain Slope Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Attenuation Slope Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Dispersion Slope Slope}$	ps/nm	0	-	-	
$\text{Polarization Dependent Delay Slope Slope}$	ps/nm	0	-	-	
$\text{Polarization Dependent Loss Slope Slope Slope}$	dB/nm	0	-	-	
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$\text{Polarization Dependent Delay Slope Slope Slope}$	ps/nm	0	-	-	
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$\text{Polarization Dependent Attenuation Slope Slope Slope Slope}$	dB/nm	0	-	-	
$\text{Polarization Dependent Dispersion Slope Slope Slope Slope}$	ps/nm	0	-	-	
$\text{Polarization Dependent Delay Slope Slope Slope Slope}$	ps/nm	0	-	-	

QSFP-DD MFm0 DoQBT/CS0 cs 0 0.239 007 9TI09 56

POWER SUPPLY FILTERING

LOW POWER MODE

LOW SPEED ELECTRICAL HARDWARE INTERFACE

ResetL	<p>> 2μs) initiates a complete module reset and</p>	InitMode	InitMode signal allows the host to define whether
ModPrsL			
IntL		SCL	board; 3 k is recommended. Note that SCL and SDA timing specifications are defined in section defined in "Management Interface Timing."
ModSeL		SDA	board; 3 k is recommended. Note that SCL and SDA timing specifications are defined in section defined in "Management Interface Timing."

					3 k pull up resistor
					1.6 k pull up resistor
				μA	
					10k pull up to Vcc

MODULE STATES BEHAVIOR

MANAGEMENT INTERFACE (I²C) TIMING

SOFT CONTROL, ALARM, AND STATUS TIMING

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